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		Application Number	10/581,076-Conf. #9470		
		Filing Date	May 31, 2006		
		First Named Inventor	Junichi Yamashita		
		Art Unit	2629		
		Examiner Name	D. L. Lewis		
Sheet	1	of	1	Attorney Docket Number	SON-3160

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
	BA	JP-54-161288	12-20-1979	Hitachi LTD et al.		
	BB	JP-05-102312	04-23-1993	NEC Corp		
	BC	JP-05-259834	10-08-1993	NEC Corp		
	BD	JP-61-206308	09-12-1986	Seiko Instr & Electronics LTD		
	BE	JP-09-223948	08-26-1997	Sharp Corp		
	BF	JP-09-200000	07-31-1997	NEC Eng LTD		
	BG	JP-2000-134067	05-12-2000	Isei Denshi Kofun Yugenkoshi		

Examiner Signature		Date Considered	
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	Razavi, B., "Design of analog CMOS integrated circuits" published, Matuzen Co., Ltd., March 30, 2003	
	CB	Japanese Action Office issued on August 14, 2008 for corresponding Japanese Application No. 2003-401274	✓

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